



SUPPLEMENTAL / BID BULLETIN NO. 001

TITLE

SUPPLY AND DELIVERY OF LAPTOP

ITB NO.

DSWD4B-2023-011

DATE

21 MARCH 2023

This Supplemental/Bid Bulletin is issued to all prospective bidders announcing the following:

PARTICULARS	CLARIFICATIONS/AMENDMENTS			
Section VII- Technical Specifications				
 Processor: CPU Architecture Technology: Processor model should at least be the latest release Base Speed Frequency: 2.5 GHz, up to 4.6 GHz Cores: ≥ 6 Cores Cache: ≥ 18MB Smart Cache 	 Processor: CPU Architecture Technology: Processor model should at least be the latest release (minimum of 11th Generation) Base Speed Frequency: 1.7GHz GHz, up to 4.6 GHz Cores: ≥ 6 Cores Cache: ≥ 12MB Smart Cache 			
8. I/O Ports: All ports should be INTEGRATED/BUILT-IN (no USB conversions /extension/adapters) 1. ≥ 2 x USB 3.0 port	8. I/O Ports: All ports should be INTEGRATED/BUILT-IN (no USB conversions /extension/adapters) 1. ≥ 2 x USB 3.0 port (Mixed Type A and Type C)			
h. Manufacturers offered model should not be custom made or made to order just to comply with the specifications	To delete this requirement: h. Manufacturers offered model should not be custom made or made to order just to comply with the specifications			
Section VI- Schedule of Requirements Delivery Schedule 45 Calendar Days upon issuance of Notice to Proceed	Section VI- Schedule of Requirements Delivery Schedule 65 Calendar Days upon issuance of Notice to Proceed			



This Bid Bulletin shall form part of the bidding documents.

Please be guided accordingly.

GENELIZA Q. GABILAN Chairperson Bids and Awards Committee

by EDITHA B. OCAMPO
Vice Chairperson

Please acco	omplish imaropa@d	the swd.gov.p	portion	below	and	email	at
Received from D Supply and Deliv	SWD Field very of Lap	Office Motop with	IMAROPA, S Project ID No	upplementa : DSWD4B-2	/Bid Bulle 2023-011	etin No.1	for the
RECEIVED BY	:	(SIGNATURE	OVER PRINTED	NAME)	DATE:		
DESIGNATION	•			**			
COMPANY							